INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number		10001477		
Filing Date		2001-11-01		
First Named Inventor Steve		Roe		
Art Unit		2123		
Examiner Name Jason		n Scott Proctor		
Attorney Docket Number		CD01203M		

/JP/	4	USPTO Notice of Aliowance for Application Number 10/001,477 (CD01203M) dated 12/04/2009; 7 pages	
/JP/	2	USPTO Notice of Allowance for Application Number 09/989,765 (CD01194M) dated 12/022/2009; 8 pages	
/JP/	3	USPTO Notice of Allowance for Application Number 09/994,600 (CD01174M) dated 01/04/2010; 4 pages	
/JP/	4	USPTO Notice of Allowance for Application Number 10/002,217 (CD01207M) dated 01/11/2010; 7 pages	
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/JP/	9	USPTO Final Rejection for Application Number 09/994,601 (CD01170M) dated 01/05/2010; 13 pages	
/JP/	10	USPTO Non-Final Rejection for Application Number 09/998,848 (CD01177M) dated 12/10/2009; 16 pages	
/JP/	444	USPTO Notice of Allowance for Application Number 10/008,096 (CD01212M) dated 02/01/2010; 6 pages	

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/JP/	12	USPTO Non-Final Rejection for Application Number 11/201,627 (CD04109) dated 12/24/2009; 22 pages										
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/JP/	1	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," 1999, IEEE; 6 pages						
/JP/	/JP/ 2 Page Maintained by Efstathiou, "Analog Electronics: Basic Circuits of Operational Amplifiers," http://web.archive.org/web/2003030306113442 December 34, 2002, version retrieved from the Internet Archives; 10 pages December 2, 2008							
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